

General Description

It combines planar MOSFET technology with a low resistance package to provide low $R_{DS(ON)}$.

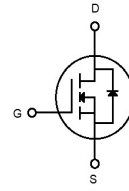
Features

- AEC-Q101 Qualified
- Low $R_{DS(ON)}$ to minimize conductive loss
- Low Gate Charge for fast switching
- Low Thermal resistance

Application

- BLDC Motor driver
- DC-DC
- Load Switch

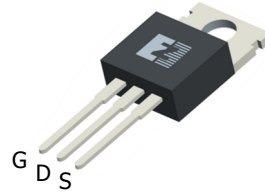
Product Summary



$$V_{DS} = 60V$$

$$R_{DS(ON)} = 11.5m\Omega$$

$$I_D = 145A$$



TO-220



Ordering Information:

Part NO.	ZMPA100N06P
Marking	ZMP100N06
Packing Information	BULK TUBE
Basic ordering unit (pcs)	1000

Absolute Maximum Ratings ($T_A=25^\circ\text{C}$, unless otherwise specified)

Parameter	Symbol	Conditions	Min.	Max.	Unit
Drain-Source Voltage	V_{DS}		-	60	V
Gate-Source Voltage ^①	V_{GS}		-20	20	V
Continuous Drain Current	I_D	$V_{GS}=10V, T_C=25^\circ\text{C}$	-	145	A
	I_D	$V_{GS}=10V, T_C=75^\circ\text{C}$	-	118	A
	I_D	$V_{GS}=10V, T_C=100^\circ\text{C}$	-	102	A
Pulsed Drain Current ^①	I_{DM}	Pulsed; $t_p \leq 10 \mu\text{s}; T_C = 25^\circ\text{C};$	-	580	A
Total Power Dissipation	P_D	$T_C=25^\circ\text{C}$	-	652	W
Total Power Dissipation	P_D	$T_A=25^\circ\text{C}$	-	2.4	W
Operating Junction Temperature	T_J		-55	175	$^\circ\text{C}$
Storage Temperature	T_{STG}		-55	175	$^\circ\text{C}$
Single Pulse Avalanche Energy	E_{AS}	$L=5mH, V_{GS}=10V, R_g=25\Omega,$	-	2560	mJ
ESD Level (HBM)	CLASS 2				

•Thermal resistance

Parameter	Symbol	Min.	Typ.	Max.	Unit
Thermal resistance, junction - case	R_{thJC}	-	-	0.23	°C/W
Thermal resistance, junction-ambient	$R_{thJA}^{②}$	-	-	62	°C/W
Soldering temperature	T_{sold}	-	-	260	°C

•Electronic Characteristics (Tj=25°C,unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Drain-Source Breakdown Voltage	BV_{DSS}	$V_{GS}=0V, I_D=250\mu A$	60	-	-	V
Gate Threshold Voltage	$V_{GS(TH)}$	$V_{GS}=V_{DS}, I_D=250\mu A$	1	1.6	2.5	V
Drain-Source Leakage Current	I_{DSS}	$V_{GS}=0V, V_{DS}=60V$	-	-	1	μA
Gate- Source Leakage Current	I_{GSS}	$V_{GS}=\pm 20V, V_{DS}=0V$	-	-	100	nA
Static Drain-source On Resistance	$R_{DS(ON)}$	$V_{GS}=10V, I_D=50A, T_J=25^\circ C$	-	11.5	13.5	m Ω
		$V_{GS}=4.5V, I_D=30A, T_J=25^\circ C$	-	16.5	20	m Ω
		$V_{GS}=10V, I_D=50A, T_J=175^\circ C$	-	26.5	-	m Ω
Forward Transconductance	g_{FS}	$V_{DS}=5V, I_{SD}=10A$	-	24	-	S
Diode Forward Voltage	V_{FSD}	$V_{GS}=0V, I_{SD}=50A$	-	-	1.3	V

•Dynamic characteristics (Tj=25°C,unless otherwise specified)

Parameter	Symbol	Condition	Min.	Typ.	Max.	Unit
Input capacitance	C_{iss}	$f=100KHz, V_{DS}=30V, V_{GS}=0V$	-	3826	-	pF
Output capacitance	C_{oss}		-	968	-	
Reverse transfer capacitance	C_{rss}		-	107	-	
Gate Resistance	R_g	$f=1MHz$	-	10	-	Ω
Total gate charge	Q_g	$V_{DD}=30V, I_D=50A, V_{GS}=10V$	-	94.8	-	nC
Gate - Source charge	Q_{gs}		-	10.8	-	
Gate - Drain charge	Q_{gd}		-	20.8	-	
Turn-ON Delay time	$t_{D(on)}$	$V_{GS}=10V, V_{DS}=30V, R_G=3.3\Omega, I_D=50A$	-	19	-	ns
Turn-ON Rise time	t_r		-	63	-	ns
Turn-Off Delay time	$t_{D(off)}$		-	93	-	ns
Turn-Off Fall time	t_f		-	28	-	ns
Reverse Recovery Time	t_{rr}	$V_{DD}=50V, di/dt=100A/\mu s, I_S=50A$	-	60	-	ns
Reverse Recovery Charge	Q_{rr}		-	165	-	nC

Fig.1 Gate-source voltage as a function of gate charge; Typical values; $T_j=25^\circ\text{C}$

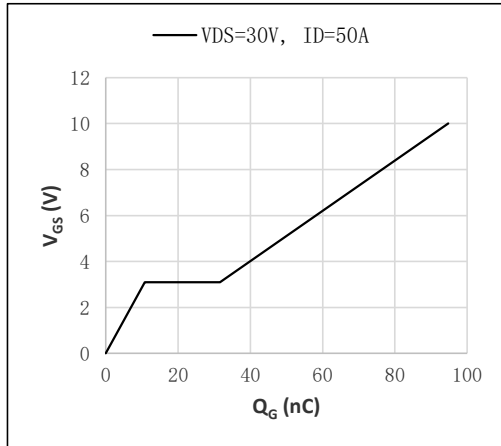


Fig.2 Input, output and reverse transfer capacitances as a function of drain-source voltage; Typical values; $T_j=25^\circ\text{C}$

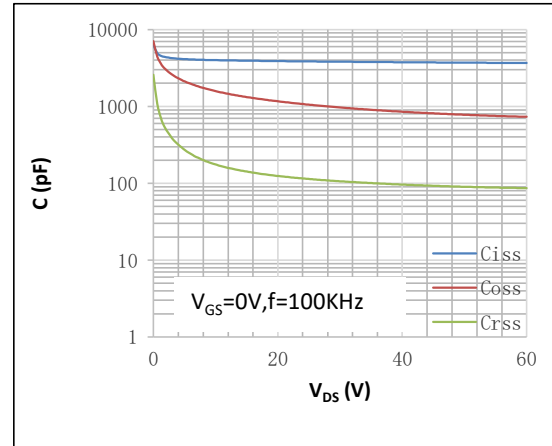


Fig.3 Output characteristics: drain current as a function of drain-source voltage; Typical values; $T_j=25^\circ\text{C}$

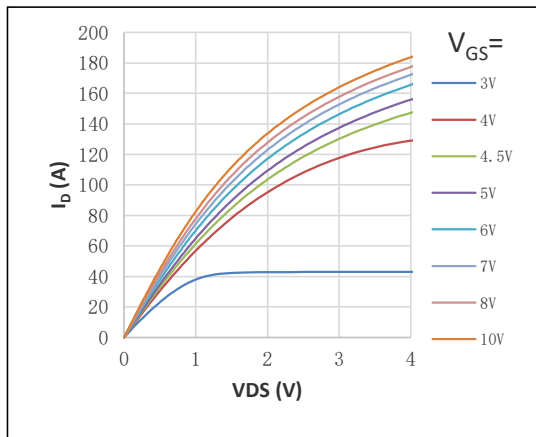


Fig.4 Output characteristics: drain current as a function of drain-source voltage; Typical values: Expanded curve; $T_j=25^\circ\text{C}$

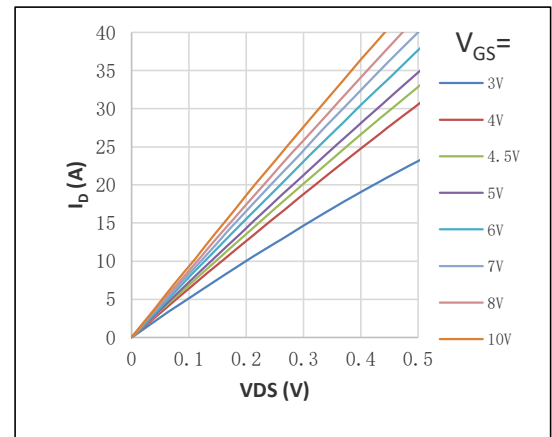


Fig.5 Gate-source threshold voltage as a function of junction temperature; Typical values

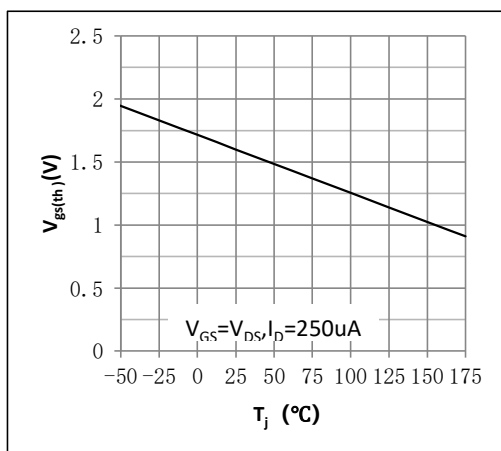


Fig.6 Drain-source on-state resistance as a function of drain current; Typical values; $T_j=25^\circ\text{C}$

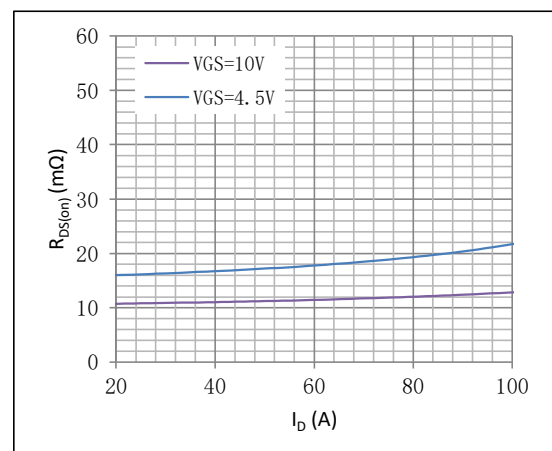


Fig.7 Drain-source on-state resistance as a function of gate-source voltage;Typical values

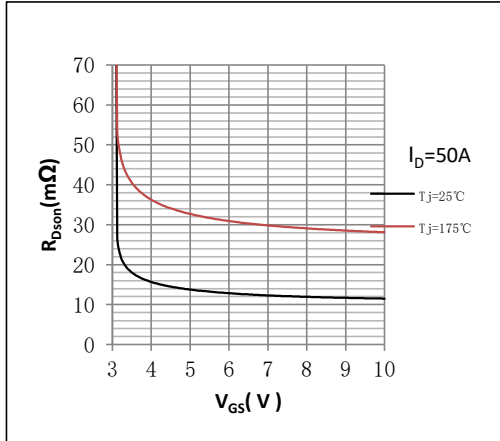


Fig.8 Normalized drain-source on-state resistance factor as a function of junction temperature;Typical values
Normalized On-Resistance= $R_{DS(on)}/R_{DS(on)}(25^{\circ}\text{C})$

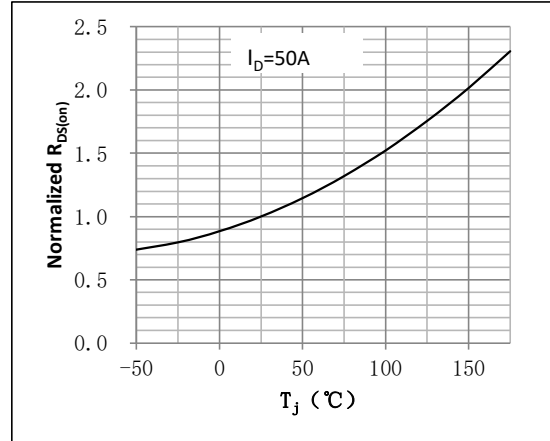


Figure 9. Source (diode forward) current as a function of source-drain (diode forward) voltage ;Typical values

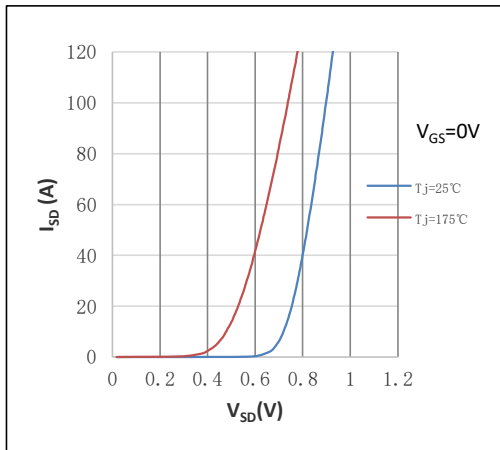


Figure 10. Transfer characteristics: drain current as a function of gate-source voltage;Typical values

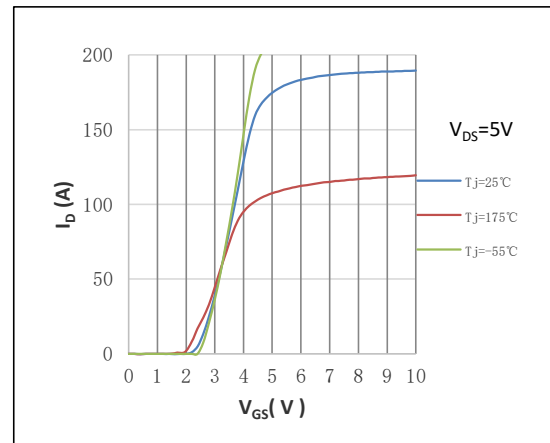


Fig.11 Safe operating area: continuous and peak drain currents as a function of drain-source voltage;Calculative values

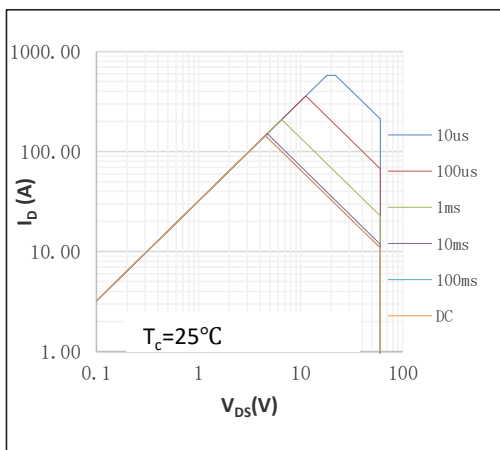


Fig.12 Continuous drain current as a function of case temperature[®];Calculative values

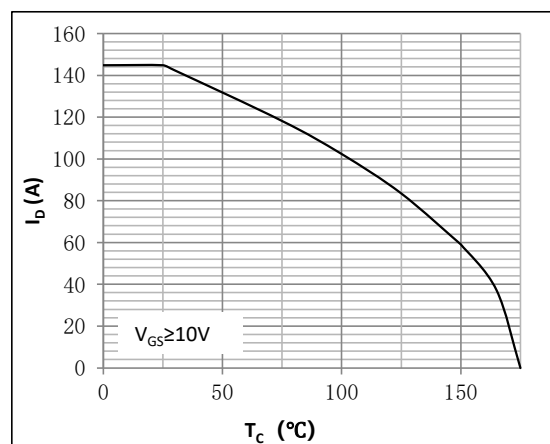


Fig.13 Drain-source breakdown voltage as a function of junction temperature; Typical values
Normalized BVDSS=BVDSS/BVDSS(25°C)

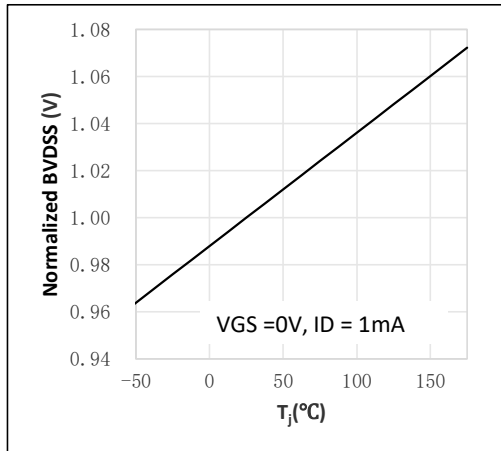


Fig.14 Normalized total power dissipation as a function of case temperature; Calculative values
Normalized Power Dissipation= $P_d/P_d(25^\circ C)$

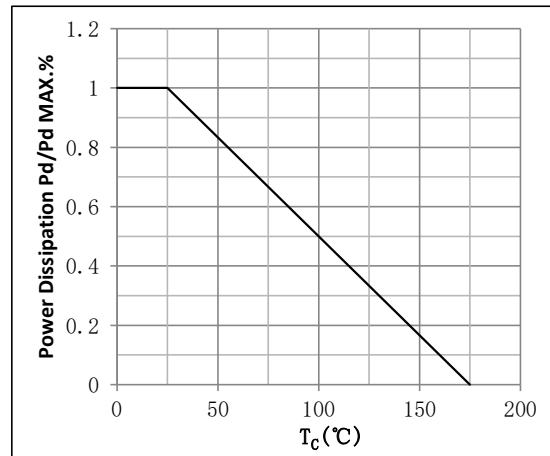
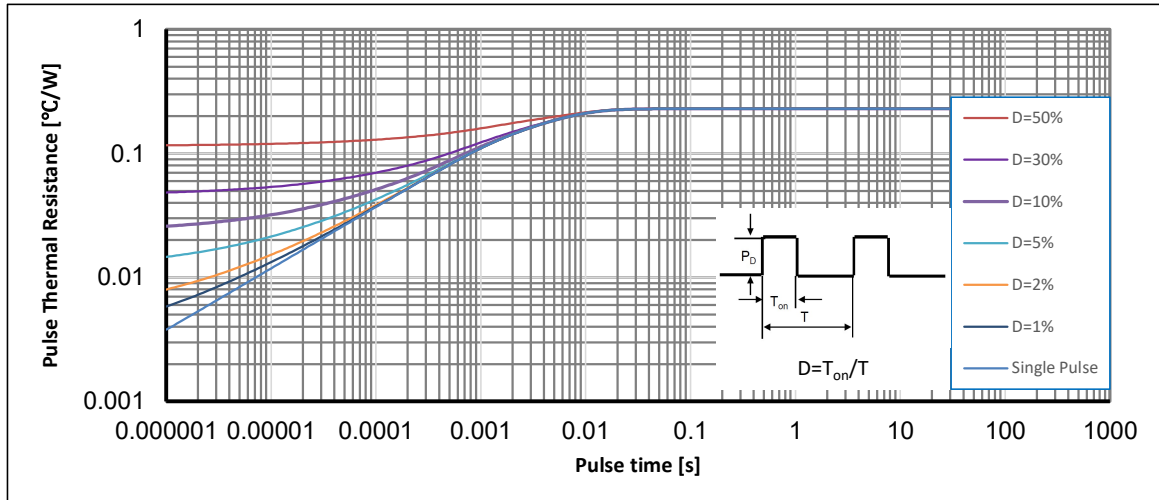
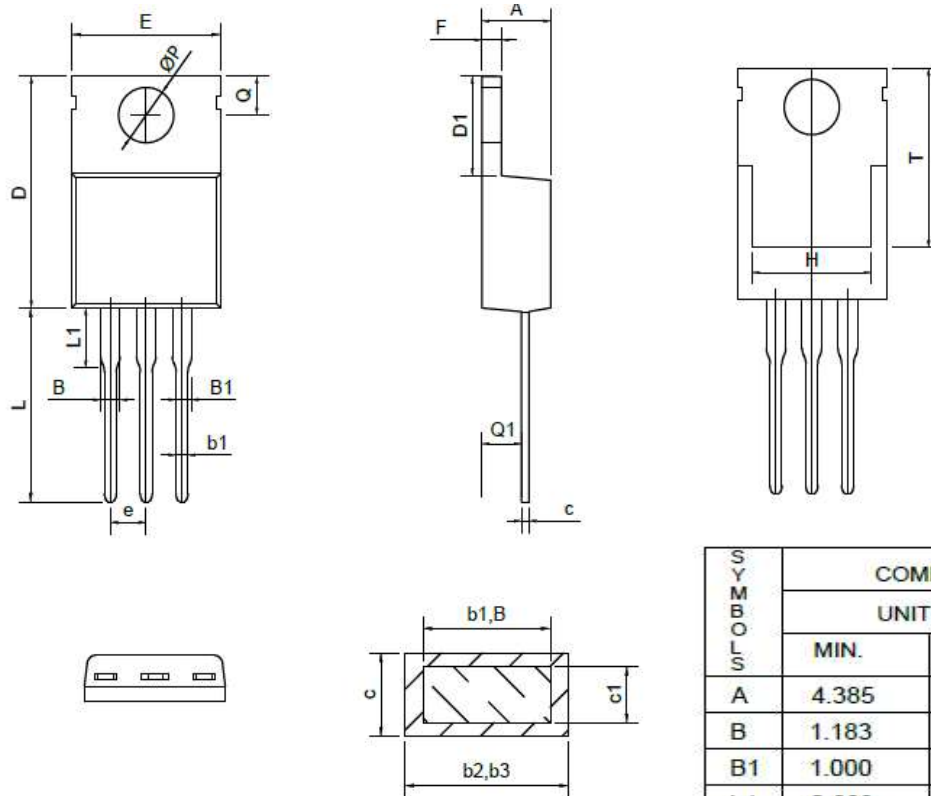


Fig.15 Transient thermal impedance from junction to case as a function of pulse duration; max values



•TO-220 Package Outline



S Y M B O L S	COMMON	
	UNIT: mm	
	MIN.	MAX.
A	4.385	4.685
B	1.183	1.478
B1	1.000	1.450
b1	0.688	1.016
b2	1.143	1.778
b3	1.143	1.727
c	0.456	0.610
c1	0.456	0.559
D	14.224	16.510
D1	5.842	6.858
E	9.685	10.385
e	2.540BSC	
F	1.200	1.400
L	12.600	14.732
L1	3.560	4.060
Q	2.500	3.048
Q1	2.032	2.921
ØP	3.600	3.900
T	12.042	12.878
H	6.858	8.890

Note:

- ① Pulse : $V_{GS}=+20V/-20V$, Duty cycle=50%, $T_j=175^{\circ}C$, $t=1000$ hours; For DC , the following test conditions can be passed: $V_{GS}=+20V/-10V$, $T_j=175^{\circ}C$, $t=1000$ hours;
- ② Device mounted on FR-4 substrate PC board, 2oz copper, with thermal bias to bottom layer 1inch square copper plate;
- ③ Practically the current will be limited by PCB, thermal design and operating temperature. $V_{GS}=10V$.

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Version	Date	Change
A	2025/5/7	New